

RF-SM-1077B1 and RF-SM-1077B2 CC1310 Series Ultra-Low-Power Sub-1 GHz Wireless Module

Version 1.0

Shenzhen RF-star Technology Co., Ltd.

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1 Device Overview

1.1 Description

RF-SM-1077B1 and RF-SM-1077B2 Sub-1 GHz modules are based on TI CC1310, which combines a flexible, very low-power RF transceiver with a powerful 48 MHz ARM® Cortex®-M3 microcontroller supporting multiple physical layers and RF standards and a dedicated radio controller Cortex®-M0 handling RF protocol commands that are stored in ROM or RAM. The modules integrate a 24.0 MHz crystal, a balun, a band pass filter, an antenna matching and an IPEX antenna matching which make the module low cost, low power consumption and long wireless communication in Sub-1 GHz. For the working frequency, RF-SM-1077B1 supports 868 MHz and 915 MHz; RF-SM-1077B2 supports 433 MHz and 470 MHz. Meanwhile, RF-star supplies the transparent transmission protocol for easing your development.

1.2 Key Features

- Microcontroller
 - Powerful ARM® Cortex®-M3 processor
 - EEMBC CoreMark® Score: 142
 - EEMBC ULPBench™ score: 158
 - Clock speed up to 48 MHz
 - 128 KB of in-system programmable flash
 - 8 KB of SRAM for Cache (or as general-purpose RAM)
 - 20 KB of ultra-low-leakage SRAM
 - 2-pin cJTAG and JTAG debugging
- Supports over-the-air (OTA) update
- On-Chip Internal DC/DC Converter
- Ultra-Low-Power Sensor Controller
 - Can run autonomously from the rest of the
 - System
 - 16-bit architecture
 - 2 KB of ultra-low-leakage SRAM for code and data
- RF-Section
 - Excellent receiver sensitivity: -124 dBm @ longrange mode, -110 dBm @ 50 kbps
 - Excellent selectivity (±100 kHz): 56 dB
 - Excellent blocking performance (±10 MHz): 90 dB
 - Programmable output power:
 - Up to +14 dBm (RF-SM-1077B1)
 - Up to +15 dBm (RF-SM-1077B2)
 - Differential RF interface

- Wireless M-Bus (EN 13757-4) and IEEE[®] 802.15.4g PHY
- Peripherals
 - All digital peripheral pins can be routed to any GPIO
 - Four general-purpose timer modules (eight 16-bit or four 32-bit timers, PWM each)
 - 12-bit ADC, 200 ksamples/s, 8-channel analog
 MUX
 - Continuous time comparator
 - Ultra-low-power clocked comparator
 - Programmable current source
 - UART
 - 2× SSI (SPI, MICROWIRE, TI)
 - I2C, I2S
 - Real-time clock (RTC)
 - AES-128 security module
 - True random number generator (TRNG)
 - Support for eight capacitive sensing buttons
 - Integrated temperature sensor
- Low Power
 - Wide supply voltage range: 1.8 V to 3.8 V
 - RX: 5.4 mA
 - TX at +10 dBm: 13.4 mA
 - Active-mode MCU 48 MHz running Coremark: 2.5 mA (51 μA/MHz)



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- Active-mode MCU: 48.5 CoreMark/mA
- Active-mode sensor controller at 24 MHz: 0.4 mA
 + 8.2 μA/MHz
- Sensor controller, one wake-up every second
- performing one 12-bit ADC sampling: 0.95 µA
- Standby: 0.7 μA (RTC running and RAM and CPU retention)
- Shutdown: 185 nA (wakeup on external events)

1.3 Applications

- 868-, 915-MHz ISM and SRD systems
- Low-power wireless systems with 50-kHz to 5-MHz channel spacing
- Home and building automation
- · Wireless alarm and security systems
- Industrial Monitoring and Control
- · Smart grid and automatic meter reading
- · Wireless healthcare applications
- Wireless sensor networks

- Active RFID
- IEEE 802.15.4g, IP-enabled smart objects (6LoWPAN), wireless M-Bus, KNX systems, Wi-SUN™, and proprietary systems
- Energy-harvesting applications
- Electronic shelf label (ESL)
- Long-range sensor applications
- Heat-cost allocators

1.4 Functional Block Diagram

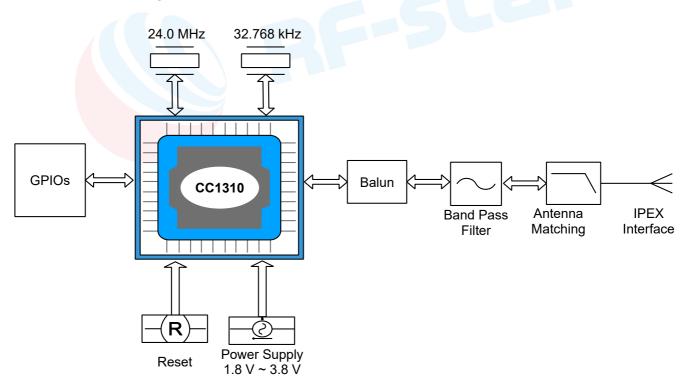


Figure 1. Functional Block Diagram of RF-SM-1077B2



1.5 Working Mode

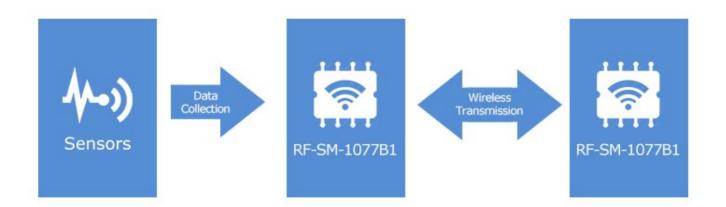


Figure 2. Working Mode of RF-SM-1077B1

1.6 Part Number Conventions

The part numbers are of the form of RF-SM-1077B1 where the fields are defined as follows:

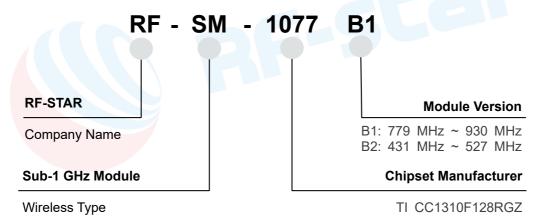


Figure 3. Part Number Conventions of RF-SM-1077B1



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2 Module Configuration and Functions

2.1 Module Parameters

Table 1. Parameters of RF-SM-1077B1/B2

Chipset	CC1310F128RGZ
Supply Power Voltage	1.8 V ~ 3.8 V, recommended to 3.3 V
Fraguenay	RF-SM-1077B1: 790 MHz ~ 930 MHz
Frequency	RF-SM-1077B2: 431 MHz ~ 527 MHz
Maximum Transmit Power	RF-SM-1077B1: +14.0 dBm
Maximum Hansilit Fower	RF-SM-1077B2: +15.0 dBm
Receiving Sensitivity	-124.0 dBm (@long range mode)
receiving densitivity	-110.0 dBm (@50 kpbs)
GPIO	30
Crystal	24 MHz, 32.768 kHz
RAM	20 KB
Flash	128 KB
Package	SMT packaging (1.27-mm half-hole pitch stamp stick)
Frequency Error	±20 kHz
Dimension	26.0 mm x 18.0 mm x 2.3 mm
Type of Antenna	IPEX connector / half-hole RF interface
Operating Temperature	-40 °C ~ +85 °C
Storage Temperature	-40 °C ~ +125 °C



2.2 Module Pin Diagram

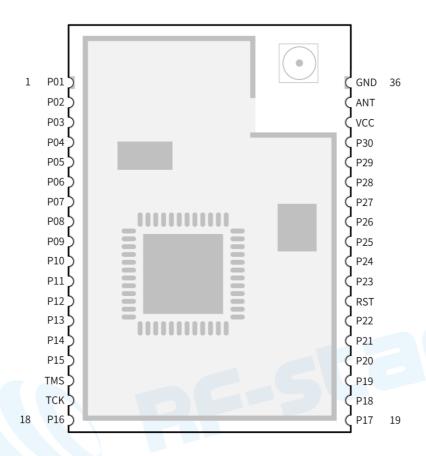


Figure 4. Pin Diagram of RF-SM-1077B1/B2

2.3 Pin Functions

Table 2. Pin Functions of RF-SM-1077B1/B2

Pin	Name	Chip Pin	Pin Type Description	
1	P01	DIO_1	Digital I/O	GPIO, Sensor Controller
2	P02	DIO_2	Digital I/O	GPIO, Sensor Controller
3	P03	DIO_3	Digital I/O	GPIO, Sensor Controller
4	P04	DIO_4	Digital I/O	GPIO, Sensor Controller
5	P05	DIO_5	Digital I/O	GPIO, Sensor Controller, high-drive capability
6	P06	DIO_6	Digital I/O	GPIO, Sensor Controller, high-drive capability
7	P07	DIO_7	Digital I/O	GPIO, Sensor Controller, high-drive capability
8	P08	DIO_8	Digital I/O	GPIO
9	P09	DIO_9	Digital I/O	GPIO



	I			
10	P10	DIO_10	Digital I/O	GPIO
11	P11	DIO_11	Digital I/O	GPIO
12	P12	DIO_12	Digital I/O	GPIO
13	P13	DIO_13	Digital I/O	GPIO
14	P14	DIO_14	Digital I/O	GPIO
15	P15	DIO_15	Digital I/O	GPIO
16	TMS	JTAG_TMSC	Digital I/O	JTAG TMSC, high-drive capability
17	TCK	JTAG_TCKC	Digital I/O	JTAG TCK
18	P16	DIO_16	Digital I/O	GPIO, JTAG_TDO, high-drive capability
19	P17	DIO_17	Digital I/O	GPIO, JTAG_TDI, high-drive capability
20	P18	DIO_18	Digital I/O	GPIO
21	P19	DIO_19	Digital I/O	GPIO
22	P20	DIO_20	Digital I/O	GPIO
23	P21	DIO_21	Digital I/O	GPIO
24	P <mark>22</mark>	DIO_22	Digital I/O	GPIO
25	RESET	RESET_N	-	Reset, active low. No internal pullup.
26	P23/ADC0	DIO_23	Digital or analog I/O	GPIO, Sensor Controller, analog
27	P24/ADC1	DIO_24	Digital or analog I/O	GPIO, Sensor Controller, analog
28	P25	DIO_25	Digital or analog I/O	GPIO, Sensor Controller, analog
29	P26	DIO_26	Digital or analog I/O	GPIO, Sensor Controller, analog
30	P27	DIO_27	Digital or analog I/O	GPIO, Sensor Controller, analog
31	P28	DIO_28	Digital or analog I/O	GPIO, Sensor Controller, analog
32	P29	DIO_29	Digital or analog I/O	GPIO, Sensor Controller, analog
33	P30	DIO_30	Digital or analog I/O	GPIO, Sensor Controller, analog
34	VCC	VCC	-	1.8 V ~ 3.8 V, recommended to 3.3 V
35	ANT	-	-	External antenna pin
36	GND	GND	-	Ground



3 Specifications

3.1 Recommended Operating Conditions

Functional operation does not guarantee performance beyond the limits of the conditional parameter values in the table below. Long-term work beyond this limit will affect the reliability of the module more or less.

Table 3. Recommended Operating Conditions of RF-SM-1077B1/B2

Items	Condition	Min.	Тур.	Max.	Unit
Operating Supply Voltage	Battery Mode	1.8	3.3	3.8	V
Operating Temperature	1	-40	+25	+85	$^{\circ}$
Environmental Hot Pendulum	1	-20		+20	°C/min

3.2 Handling Ratings

Table 4. Handling Ratings of RF-SM-1077B1/B2

Items	Condition	Min.	Тур.	Max.	Unit
Storage Temperature	Tstg	-40	+25	+125	$^{\circ}$
Human Body Model	НВМ		±3000		V
Moisture Sensitivity Level			3		
Charged Device Model			±500		V

3.3 Power Consumption Test

Table 5. Power Consumption of RF-SM-1077B1/B2

Mode	Min. Current	Max. Current	Average Current	Samples
Idle	1.29 mA	1.32 mA	1.31 mA	5000
Standby	68 nA	69.2 µA	980 nA	5000
Shutdown	52 nA	88 nA	79 nA	5000
RF-RX	5.2 mA	5.7 mA	5.4 mA	5000
RF-TX	-	25.2 mA (14 dBm)	-	-

Note:

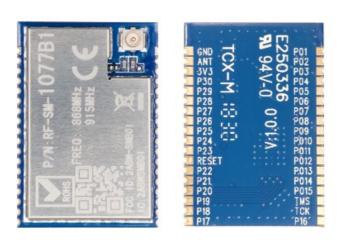
Test conditions: battery power supply: 2.88 V. Test instrument: RIGOL DM3068 digital multimeter.

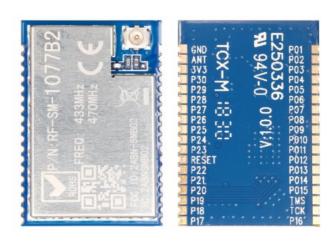
Data of RF-TX only provide peak value as a reference, the average power is proportional to the frequency of data transmission.



4 Application, Implementation, and Layout

4.1 Module Photos





RF-SM-1077B1

RF-SM-1077B2

Figure 5. Photos of RF-SM-1077B1/B2

4.2 Recommended PCB Footprint

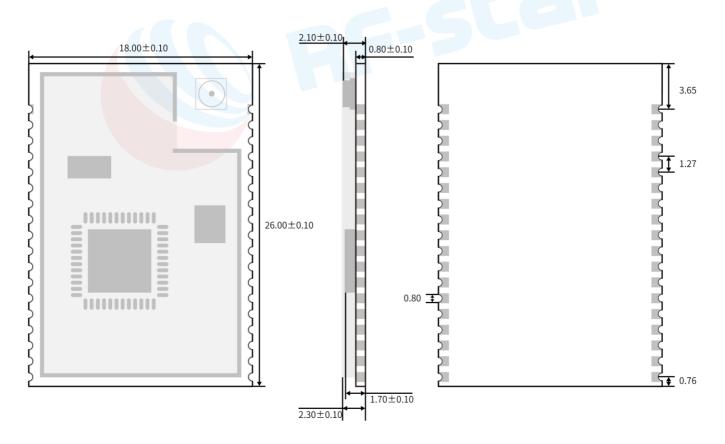


Figure 6. Recommended PCB Footprint of RF-SM-1077B1 (mm)



4.3 Schematic Diagram

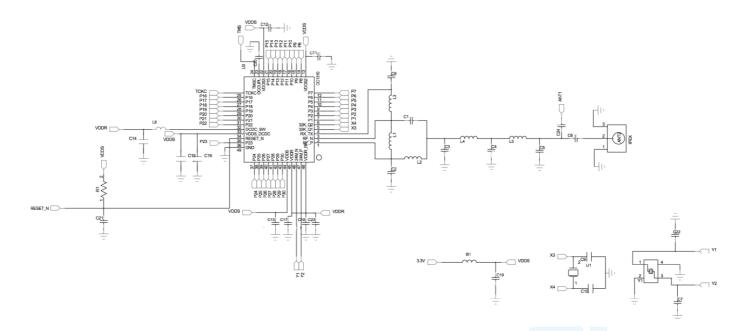


Figure 7. Schematic Diagram of RF-SM-1077B1/B2

4.4 Antenna

4.4.1 Antenna Design Recommendation

- 1. The antenna installation structure has a great influence on the module performance. It is necessary to ensure the antenna is exposed and preferably vertically upward. When the module is installed inside of the case, a high-quality antenna extension wire can be used to extend the antenna to the outside of the case.
- 2. The antenna must not be installed inside the metal case, which will cause the transmission distance to be greatly weakened.

4.4.2 Antenna Output Mode Modification

1. RF-SM-1077B1/B2 has two antenna output modes. The one is IPEX connector and the other is a stamp half-hole output (ANT pin, see pin function table for details).

The default delivery is the **IPEX connector**, and the capacitor connected to the IPEX is welded. If you want to use the external antenna by the ANT pin, the capacitor position should be removed to the right solder joint to have the access to the ANT pin. The location of the capacitor is shown in the figure below.

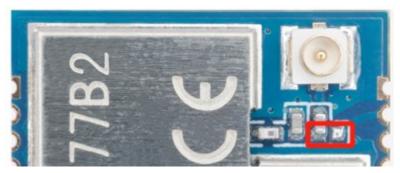


Figure 8. Antenna Output Mode Change of RF-SM-1077B1/B2

5.4.3 External Antenna Design Recommendation of the Half-Hole ANT Pin

1. A Π -type matching circuit is reserved for the antenna, and 50 Ω impedance control is performed on the RF traces. The traces are as short as possible, and 135° or arc traces are used as much as possible. No vias are used to change layers. More GND vias are placed around the RF traces.

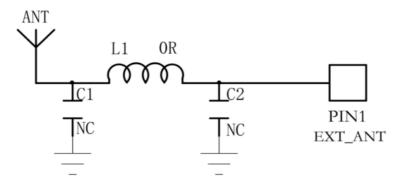


Figure 9. Reference Design of the External Antenna

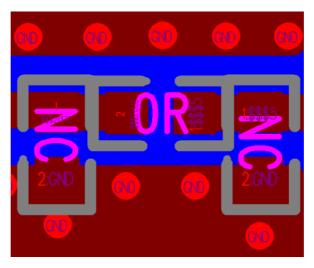


Figure 10. Reference Design of the External Antenna Traces



2. The RF trace width and copper-clad spacing can be calculated by SI9000 software, and the impedance is controlled to 50 Ω according to the actual board thickness, number of layers, plate, dielectric thickness, dielectric constant, copper thickness, line width, line spacing, and solder mask thickness.

Example: FR4 is a double-layer board with a thickness of 1.0 mm. Through calculation, the width of the trace is 0.8254 mm, and the spacing between traces and copper is 0.22 mm.

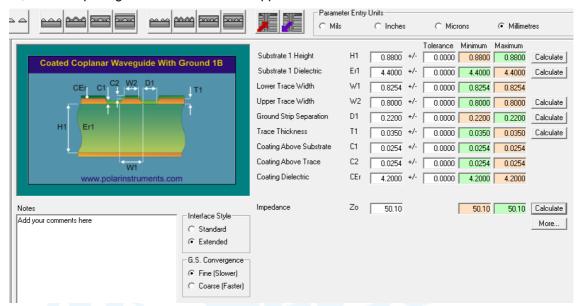


Figure 11. SI9000 Impedance Calculation Diagram

5.4.4 IPEX Connector Specification

RF-SM-1077B1/B2 module is integrated the IPEX version 1 antenna seat, the specification of the antenna seat is as follows:

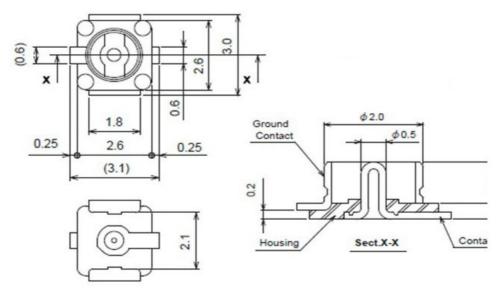


Figure 12. Specification of Antenna Seat



The specification of the IPEX wire end is as follows:

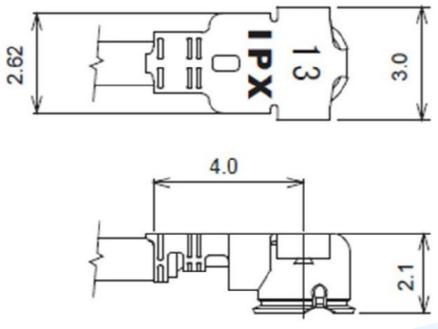


Figure 13. Specification of IPEX Wire

4.5 Basic Operation of Hardware Design

- 3. It is recommended to offer the module with a DC stabilized power supply, a tiny power supply ripple coefficient and the reliable ground. Please pay attention to the correct connection between the positive and negative poles of the power supply. Otherwise, the reverse connection may cause permanent damage to the module;
- 4. Please ensure the supply voltage is between the recommended values. The module will be permanently damaged if the voltage exceeds the maximum value. Please ensure the stable power supply and no frequently fluctuated voltage.
- 5. When designing the power supply circuit for the module, it is recommended to reserve more than 30% of the margin, which is beneficial to the long-term stable operation of the whole machine. The module should be far away from the power electromagnetic, transformer, high-frequency wiring and other parts with large electromagnetic interference.
- 6. The bottom of module should avoid high-frequency digital routing, high-frequency analog routing and power routing. If it has to route the wire on the bottom of module, for example, it is assumed that the module is soldered to the Top Layer, the copper must be spread on the connection part of the top layer and the module, and be close to the digital part of module and routed in the Bottom Layer (all copper is well grounded).
- 7. Assuming that the module is soldered or placed in the Top Layer, it is also wrong to randomly route the Bottom Layer or other layers, which will affect the spurs and receiving sensitivity of the module to some degrees;
- 8. Assuming that there are devices with large electromagnetic interference around the module, which will greatly affect the module performance. It is recommended to stay away from the module according to the strength of the interference. If circumstances permit, appropriate isolation and shielding can be done.



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- 9. Assuming that there are routings of large electromagnetic interference around the module (high-frequency digital, high-frequency analog, power routings), which will also greatly affect the module performance. It is recommended to stay away from the module according to the strength of the interference. If circumstances permit, appropriate isolation and shielding can be done.
- 10. It is recommended to stay away from the devices whose TTL protocol is the same 868 MHz physical layer.
- 11. The antenna installation structure has a great influence on the module performance. It is necessary to ensure the antenna is exposed and preferably vertically upward. When the module is installed inside of the case, a high-quality antenna extension wire can be used to extend the antenna to the outside of the case.
- 12. The antenna must not be installed inside the metal case, which will cause the transmission distance to be greatly weakened.

4.6 Trouble Shooting

4.6.1 Unsatisfactory Transmission Distance

- When there is a linear communication obstacle, the communication distance will be correspondingly weakened.
 Temperature, humidity, and co-channel interference will lead to an increase in communication packet loss rate. The
 performances of ground absorption and reflection of radio waves will be poor, when the module is tested close to
 the ground.
- 2. Seawater has a strong ability to absorb radio waves, so the test results by seaside are poor.
- 3. The signal attenuation will be very obvious, if there is a metal near the antenna or the module is placed inside of the metal shell.
- 4. The incorrect power register set or the high data rate in an open air may shorten the communication distance. The higher the data rate, the closer the distance.
- 5. The low voltage of the power supply is lower than the recommended value at ambient temperature, and the lower the voltage, the smaller the power is.
- 6. The unmatchable antennas and module or the poor quality of antenna will affect the communication distance.

4.6.2 Vulnerable Module

- Please ensure the supply voltage is between the recommended values. The module will be permanently damaged
 if the voltage exceeds the maximum value. Please ensure the stable power supply and no frequently fluctuated
 voltage.
- 2. Please ensure the anti-static installation and the electrostatic sensitivity of high-frequency devices.
- 3. Due to some humidity sensitive components, please ensure the suitable humidity during installation and application.

 If there is no special demand, it is not recommended to use at too high or too low temperature.



4.6.3 High Bit Error Rate

- 1. There are co-channel signal interferences nearby. It is recommended to be away from the interference sources or modify the frequency and channel to avoid interferences.
- 2. The unsatisfactory power supply may also cause garbled. It is necessary to ensure the power supply reliability.
- 3. If the extension wire or feeder wire is of poor quality or too long, the bit error rate will be high.

4.7 Electrostatics Discharge Warnings

The module will be damaged for the discharge of static. RF-star suggest that all modules should follow the 3 precautions below:

- 1. According to the anti-static measures, bare hands are not allowed to touch modules.
- 2. Modules must be placed in anti- static areas.
- 3. Take the anti-static circuitry (when inputting HV or VHF) into consideration in product design.

 Static may result in the degradation in performance of module, even causing the failure.

4.8 Soldering and Reflow Condition

- 1. Heating method: Conventional Convection or IR/convection.
- 2. Solder paste composition: Sn96.5 / Ag3.0 / Cu0.5
- 3. Allowable reflow soldering times: 2 times based on the following reflow soldering profile.
- 4. Temperature profile: Reflow soldering shall be done according to the following temperature profile.
- 5. Peak temperature: 245 °C.

Table 6. Temperature Table of Soldering and Reflow

Profile Feature	Sn-Pb Assembly	Pb-Free Assembly	
Solder Boots	000 / DL 07	Sn96.5 / Ag3.0 /	
Solder Paste	Sn63 / Pb37	Cu0.5	
Min. Preheating Temperature (Tmin)	100 ℃	150 ℃	
Max. Preheating Temperature (T _{max})	150 ℃	200 ℃	
Preheating Time (T _{min} to T _{max}) (t ₁)	60 s ~ 120 s	60 s ~ 120 s	
Average Ascend Rate (T _{max} to T _p)	Max. 3 °C/s	Max. 3 °C/s	
Liquid Temperature (T∟)	183 ℃	217 ℃	
Time above Liquidus (t∟)	60 s ~ 90 s	30 s ~ 90 s	
Peak Temperature (Tp)	220 ℃ ~235 ℃	230 ℃ ~250 ℃	
Average Descend Rate (Tp to Tmax)	Max. 6 °C/s	Max. 6 ℃/s	
Time from 25 $^{\circ}\!$	Max. 6 minutes	Max. 8 minutes	



Time of Soldering Zone (t_P) 20±10 s 20±10 s

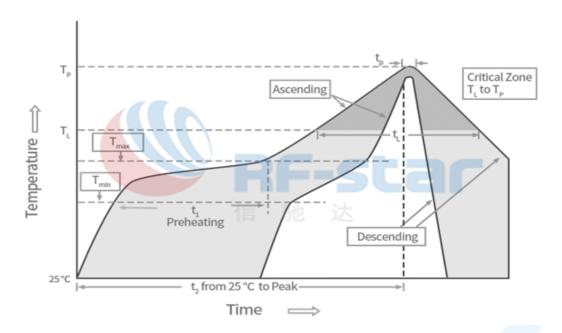


Figure 14. Recommended Reflow for Lead Free Solder



5 Optional Package Specification

The default package method is **by tray**. If you need the modules to be shipped by tape & reel, pls contact us in advance.

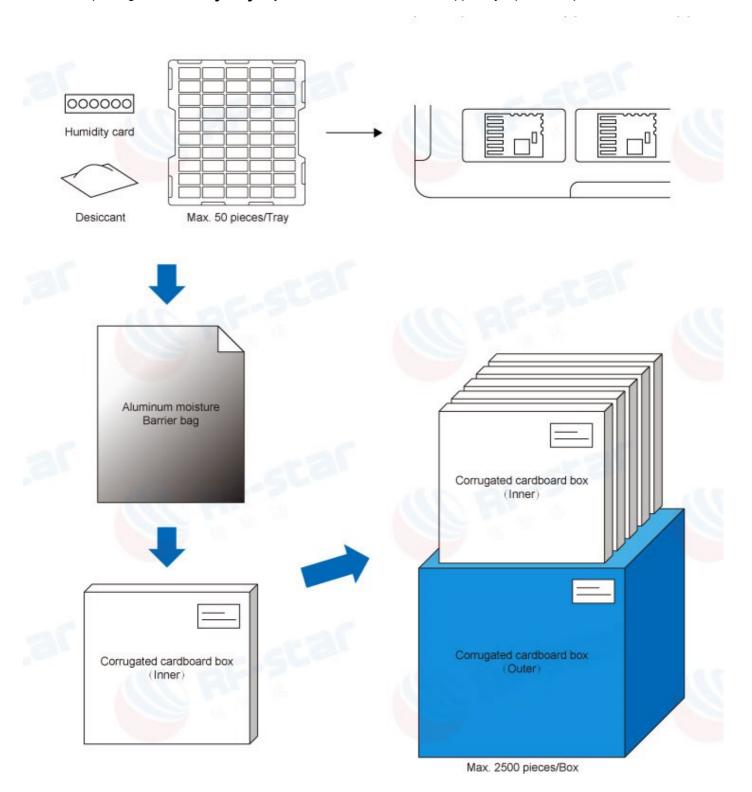


Figure 15. Default Package by Tray



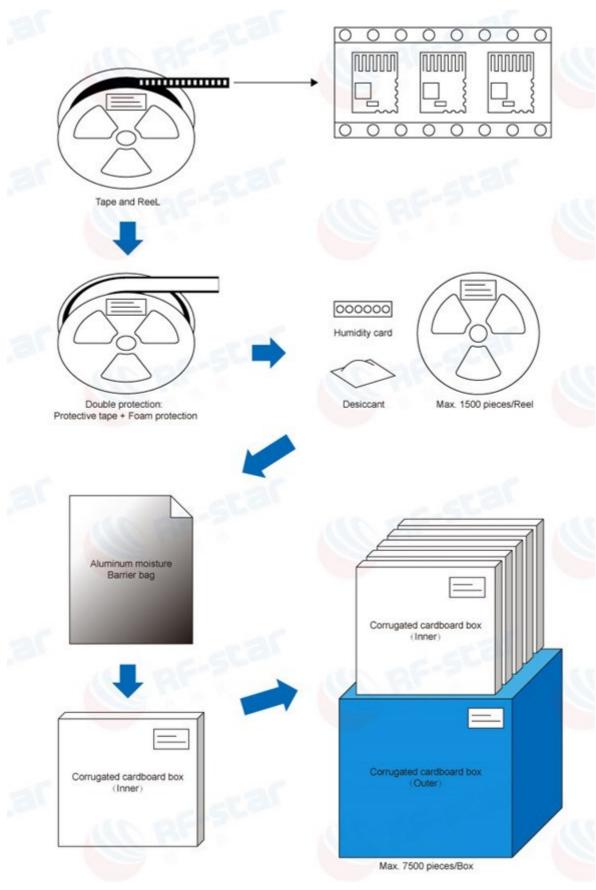


Figure 16. Package by Tape & Reel



6 Certification

5.1 Reach

Reach Certificate No.: C181213010001



Figure 17. SRRC Certificate

5.2 RoHS

RoHS Certificate No.: C210906053003-1



Figure 18. RoHS Certificate



7 Revision History

Date	Version No.	Description
2017.10.19	V1.0	The initial version is released.
2018.03.12	V1.0	Add commands.
2018.01.24	V1.0	Add inquiry MAC address function.
2018.08.02	V1.0	Update module datasheet.
2022 05 26	V/1.0	Update MSL level.
2023.05.26	V1.0	Update the Shenzhen office address.

Note:

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